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BACK SIDE CHARGE TRAPPING NANO-SCALE SILICON NON-VOLATILE ...

Back side charge trapping non-volatile memory devices were demonstrated for the first time. The fabrication process is described and the electrical characteristics are

Charge-Trapping Phenomena in HfO₂-Based FeFET-Type ...

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Investigation of multilayer WS₂ flakes as charge trapping ...

In this study, the non-volatile flash memory devices utilize tungsten sulfide flakes as the charge trapping stack layers were fabricated. The sandwiched structure of